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Toledo, S.;

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1 Academic papers: Scenario-based training for deception detection

David P. Biros

 October 2004 **Proceedings of the 1st annual conference on Information security curriculum development**

 Full text available: [pdf\(165.08 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Computer defense is not perfect. Each year hackers exploit hundreds of vulnerabilities in information systems even though millions of dollars are spent on information security. Firewalls, intrusion detection systems and patch management software all help to increase systems security, but they are only perimeter defenses. Once inside, a hacker can do significant damage and the vulnerability to deception is great. At that point, users become the last line of defense. This paper describes two field ...

Keywords: deception detection, information security, information security training, intrusion detection

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61 [Performance prediction of parallel systems with scalable specifications—methodology and case study](#)



H. Wabnig, G. Haring

April 1995 **ACM SIGMETRICS Performance Evaluation Review**, Volume 22 Issue 2-4

Full text available: [pdf\(1.34 MB\)](#)

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This paper describes the general methodology of specifying parallel systems within the PAPS (Performance Analysis of Parallel Systems) toolset and presents a case study that shows the applicability and accuracy of the Petri net based performance prediction tools contained in the toolset. Parallel systems are specified in the PAPS toolset by separately defining the program workload, the hardware resources, and the mapping of the program to the hardware. The resource parameterization is described ...

62 [Systems modeling with xpetri](#)



Robert Geist, Darren Crane, Stephen Daniel, Darrell Suggs

December 1994 **Proceedings of the 26th conference on Winter simulation**

Full text available: [pdf\(747.69 KB\)](#)

Additional Information: [full citation](#), [references](#), [index terms](#)

63 [Session 20: software performance: Parallel performance prediction using lost cycles analysis](#)



Mark E. Crovella, Thomas J. LeBlanc

November 1994 **Proceedings of the 1994 ACM/IEEE conference on Supercomputing**

Full text available: [pdf\(980.00 KB\)](#)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#)

Most performance debugging and tuning of parallel programs is based on the "measure-modify" approach, which is heavily dependent on detailed measurements of programs during execution. This approach is extremely time-consuming and does not lend itself to predicting performance under varying conditions. Analytic modeling and scalability analysis provide predictive power, but are not widely used in practice, due primarily to their emphasis on asymptotic behavior and the difficulty of developing acc ...

64

[Contrasting characteristics and cache performance of technical and multi-user commercial workloads](#)



Ann Marie Grizzafi Maynard, Colette M. Donnelly, Bret R. Olszewski
November 1994 **Proceedings of the sixth international conference on Architectural support for programming languages and operating systems**, Volume 29, 28 Issue 11 , 5

Full text available:  pdf(1.35 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Experience has shown that many widely used benchmarks are poor predictors of the performance of systems running commercial applications. Research into this anomaly has long been hampered by a lack of address traces from representative multi-user commercial workloads. This paper presents research, using traces of industry-standard commercial benchmarks, which examines the characteristic differences between technical and commercial workloads and illustrates how those differences affect cache ...

Keywords: cache performance, commercial workloads, memory subsystems, operating system activity, technical applications

65 A new approach to I/O performance evaluation: self-scaling I/O benchmarks, predicted I/O performance 

Peter M. Chen, David A. Patterson
November 1994 **ACM Transactions on Computer Systems (TOCS)**, Volume 12 Issue 4

Full text available:  pdf(1.86 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

Current I/O benchmarks suffer from several chronic problems: they quickly become obsolete; they do not stress the I/O system; and they do not help much in understanding I/O system performance. We propose a new approach to I/O performance analysis. First, we propose a self-scaling benchmark that dynamically adjusts aspects of its workload according to the performance characteristic of the system being measured. By doing so, the benchmark automatically scales across current and future systems ...

66 Compiling performance models from parallel programs 

Arjan J. C. van Gemund
July 1994 **Proceedings of the 8th international conference on Supercomputing**

Full text available:  pdf(1.01 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

A technique is described to automatically compile performance models in the course of program translation. The performance models are fully symbolic in order to preserve as much diagnostic information as possible. Although compiled statistically, the models account for the effects of resource contention, due to the introduction of a novel algorithm within the symbolic compilation scheme. It is shown that the compilation approach fundamentally outperforms traditional static estimation proced ...

67 The modeling methodology, model specifications and development of CASI: CASE/Architecture Simulation Integration 

Arnold J. Almanzor, Paul R. Work
December 1993 **Proceedings of the 25th conference on Winter simulation**

Full text available:  pdf(427.31 KB) Additional Information: [full citation](#), [references](#)

68 Analytical performance prediction on multicomputers 

M. J. Clement, M. J. Quinn
December 1993 **Proceedings of the 1993 ACM/IEEE conference on Supercomputing**

Full text available:  pdf(813.82 KB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

69 Hot spot analysis in large scale shared memory multiprocessors 

K. Harzallah, K. C. Sevcik

December 1993 **Proceedings of the 1993 ACM/IEEE conference on Supercomputing**

Full text available:  pdf(836.78 KB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

70 The mathematics of product form queuing networks 

Randolph D. Nelson

September 1993 **ACM Computing Surveys (CSUR)**, Volume 25 Issue 3

Full text available:  pdf(2.56 MB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

Keywords: partial balance, product form, quasireversibility, queuing theory, reversibility

71 The accuracy of trace-driven simulations of multiprocessors 

Stephen R. Goldschmidt, John L. Hennessy

June 1993 **ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 1993 ACM SIGMETRICS conference on Measurement and modeling of computer systems**, Volume 21 Issue 1

Full text available:  pdf(1.26 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

In trace-driven simulation, traces generated for one set of system characteristics are used to simulate a system with different characteristics. However, the execution path of a multiprocessor workload may depend on the order of events occurring on different processing elements. The event order, in turn, depends on system characteristics such as memory-system latencies and buffer-sizes. Trace-driven simulations of multiprocessor workloads are inaccurate unless the dependencies are eliminated from ...

72 A new approach to I/O performance evaluation: self-scaling I/O benchmarks, predicted I/O performance 

Peter M. Chen, David A. Patterson

June 1993 **ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 1993 ACM SIGMETRICS conference on Measurement and modeling of computer systems**, Volume 21 Issue 1

Full text available:  pdf(1.36 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Current I/O benchmarks suffer from several chronic problems: they quickly become obsolete, they do not stress the I/O system, and they do not help in understanding I/O system performance. We propose a new approach to I/O performance analysis. First, we propose a self-scaling benchmark that dynamically adjusts aspects of its workload according to the performance characteristic of the system being measured. By doing so, the benchmark automatically scales across current and future systems. The eval ...

73 Evaluating performance of prefetching second level caches 

Robert B. Smith, James K. Archibald, Brent E. Nelson

May 1993 **ACM SIGMETRICS Performance Evaluation Review**, Volume 20 Issue 4

Full text available: Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index](#)

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Due to the increasing disparity between processor and main memory system cycle times, many computer systems are now incorporating two levels of cache memory. Several studies have been done on the design and performance of second level caches, including [3] and [20]. It has been shown that the addition of a second level of cache enhances the performance of many systems.

74 Benchmark workload generation and performance characterization of multiprocessors 

A. Nanda, L. M. Ni

December 1992 Proceedings of the 1992 ACM/IEEE conference on SupercomputingFull text available:  pdf(954.56 KB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)**75 The winter simulation conference: perspectives of the founding fathers** 

Michel Araten, Harold G. Hixson, Austin C. Hoggatt, Philip J. Kiviat, Michael F. Morris, Arnold Ockene, Julian Reitman, Joseph M. Sussman, James R. Wilson

December 1992 Proceedings of the 24th conference on Winter simulationFull text available:  pdf(2.83 MB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)**76 Effects of time-varied arrival rates: an investigation in emergency ambulance service systems** 

Zhiwei Zhu, Mark A. McKnew, Jim Lee

December 1992 Proceedings of the 24th conference on Winter simulationFull text available:  pdf(651.50 KB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)**77 Automatic performance prediction to support parallelization of Fortran programs for massively parallel systems** 

Thomas Fahringer, Roman Blasko, Hans P. Zima

August 1992 Proceedings of the 6th international conference on SupercomputingFull text available:  pdf(917.61 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

In order to take on the challenge of fully automatic program parallelizing, one of the last and probably the most decisive missing tool is a performance estimation system. In this paper a new performance prediction tool is introduced, which automatically derives performance estimates for single program multiple data (SPMD) parallel Fortran 77 programs based on distributed memory systems (DMS). The underlying methodology is based on static and dynamic techniques. This paper discusses in part ...

78 Concurrency control for high contention environments 

Peter A. Franaszek, John T. Robinson, Alexander Thomasian

June 1992 ACM Transactions on Database Systems (TODS), Volume 17 Issue 2Full text available:  pdf(2.91 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

Future transaction processing systems may have substantially higher levels of concurrency due to reasons which include: (1) increasing disparity between processor speeds and data access latencies, (2) large numbers of processors, and (3) distributed databases. Another influence is the trend towards longer or more complex transactions. A possible consequence is substantially more data contention, which could limit total achievable throughput. In

particular, it is known that the usual locking ...

Keywords: concurrency control, transaction processing

79 [An analysis of dynamic page placement on a NUMA multiprocessor](#) 

Richard P. LaRowe, Mark A. Holliday, Carla Schlatter Ellis

June 1992 **ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 1992 ACM SIGMETRICS joint international conference on Measurement and modeling of computer systems**, Volume 20 Issue 1

Full text available:  pdf(1.08 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

The class of NUMA (nonuniform memory access time) shared memory architectures is becoming increasingly important with the desire for larger scale multiprocessors. In such machines, the placement and movement of code and data are crucial to performance. The operating system can play a role in managing placement through the policies and mechanisms of the virtual memory subsystem. In this paper, we develop an analytic model of memory system performance of a Local/Remote NUMA architecture based ...

80 [Modeling distributed file systems](#) 

Anna Hac

May 1992 **ACM SIGMETRICS Performance Evaluation Review**, Volume 19 Issue 4

Full text available:  pdf(741.69 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

This paper describes different methods and techniques used to model, analyze, evaluate and implement distributed file systems. Distributed file systems are characterized by the distributed system hardware and software architecture, in which they are implemented as well as by the file systems' functions. In addition, distributed file system performance depends on the load executed in the system. Modeling and analysis of distributed file systems requires new methods to approximate complexity of th ...

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